

IN THE CLAIMS

1-12. (Cancelled)

13. (Previously Presented) A method of processing digital communication signals in a digital communication system, the method comprising:

receiving digital chip samples;

storing even phase samples of the digital chip samples in a first buffer of a plurality of buffers of the digital communication system;

storing odd phase samples of the digital chip samples in a second buffer of the plurality of buffers;

providing the even phase digital samples or the odd phase digital samples to a demodulator, wherein the demodulator is adapted to produce a symbol estimate based on the even phase digital samples or the odd phase digital samples; and

providing other ones of the even phase digital samples or the odd phase digital samples, whichever are not used by the demodulator, to a searcher, wherein the searcher is adapted to determine multi-path components in the digital communication signals.

14. (Previously Presented) The method of claim 13, further comprising entering a power down state upon providing a sufficient number of digital samples to the searcher.

15. (Original) The method of claim 14, further comprising leaving the power down state when a new block of data is available.